IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Cho, et al.

Docket No.:

TI-36298

Filed:

Herewith

Examiner:

TBD

Serial No.:

TBD

Art Unit:

TBD

For:

Built-In Self Test Method and Apparatus for Jitter Transfer and Jitter Tolerance

Mail Stop: Patent Application Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

February 25, 2004

Date

Ira S. Matsil

Attorney for Applicant

Respectfully submitted,

Reg. No. 35,272

Slater & Matsil, L.L.P. 17950 Preston Rd., Suite 1000 Dallas, TX 75252 (972) 732-1001 (phone) (972) 732-9218 (fax)

PTO/SB/08a (08-03)
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STATEMENT BY APPLICANT				First Named Inventor	Cho, et al.	
				Art Unit	TBD	
(Use as many sheets as necessary)				Examiner Name	TBD	
Sheet	1	of	1	Attorney Docket Number	TI-36298	

U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ^{2 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	Α	US-6,397,042 B1	05-28-2002	Prentice, et al.			
	В	US-6,433,599 B2	08-13-2002	Friedrich, et al.			
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FOREIGN PATENT DOCUMENTS							
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